(54) Title: ADDRESS ENCRYPTION METHOD FOR FLASH MEMORIES
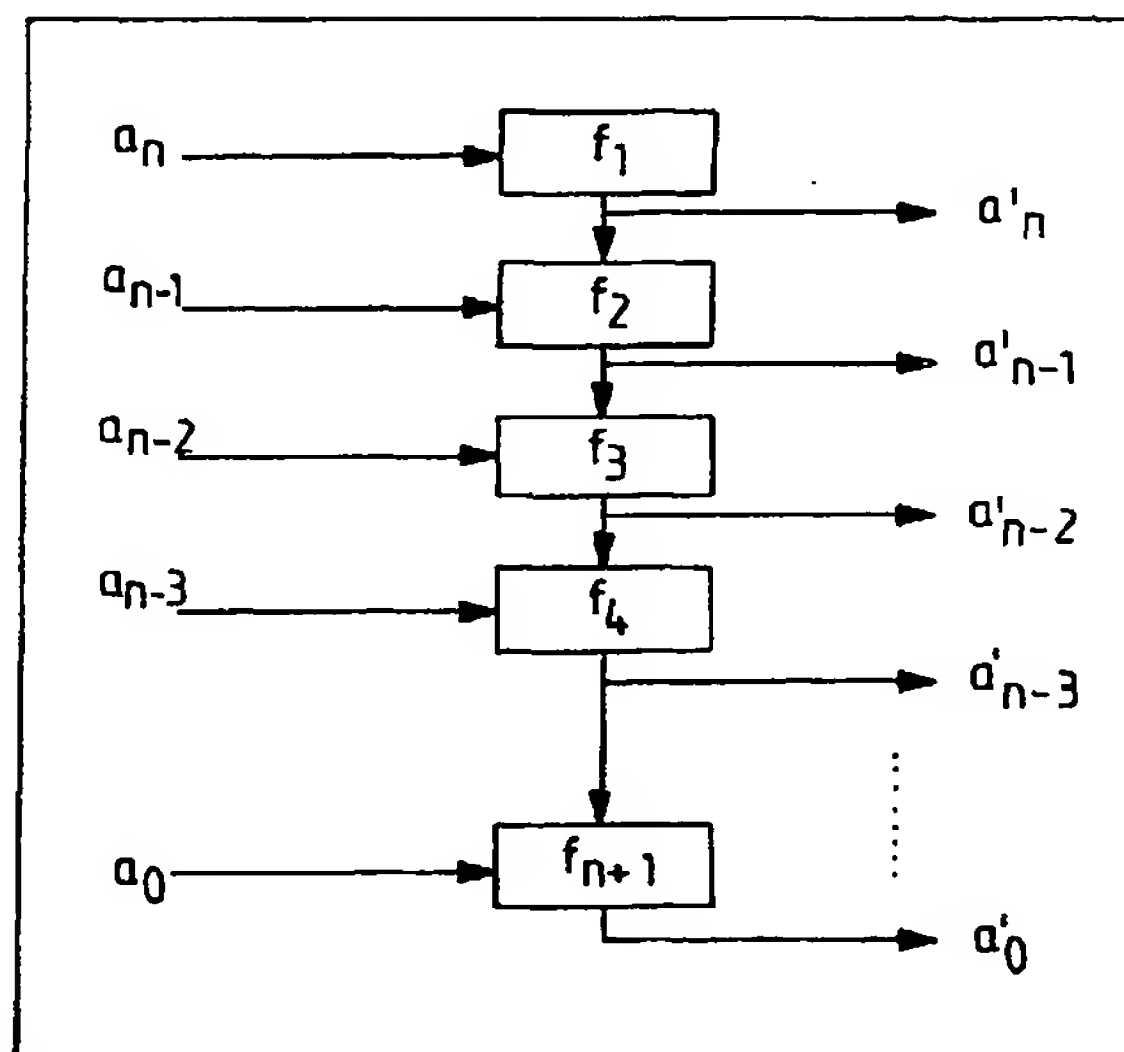
(57) Abstract: In order to further develop a data processing device, in particular an electronic memory component, comprising a plurality of access-secured sub-areas, in particular a plurality of access-secured memory areas, each having at least one assigned parameter (a_n, a_{n-1},..., a_1, a_0), in particular address, and a method of encrypting at least one parameter (a_n, a_{n-1},..., a_1, a_0), in particular the address, of at least one access-secured sub-area, in particular at least one access-secured memory area, of at least one data processing device, in particular at least one electronic memory component, in such a way that on the one hand the security of such devices is increased considerably and on the other hand the associated expense and technical complexity are not too great, it is proposed that the parameter (a_n, a_{n-1},..., a_1, a_0) of at least one sub-area be capable of encryption only in certain areas, i.e. in dependence on at least one further sub-area (a'_n, a'_{n-1},..., a'_1, a'_0).